

ABSTRACT OF THE DISCLOSURE

5 A switch/network adapter port ("SNAP") for clustered computers employing multi-adaptive processor ("MAPTM", a trademark of SRC Computers, Inc.) elements in a dual in-line memory module ("DIMM") or RambusTM in-line memory module ("RIMM") format to significantly enhance data transfer rates over that otherwise available through use of the standard peripheral component interconnect ("PCI") bus. Particularly
10 disclosed is a microprocessor based computer system utilizing either a DIMM or RIMM physical format processor element for the purpose of implementing a connection to an external switch, network, or other device. In a particular embodiment, connections may
15 be provided to either the PCI, accelerated graphics port ("AGP") or system maintenance ("SM") bus for purposes of passing control information to the host microprocessor or other control chips. The field programmable gate array ("FPGA") based processing
20 elements have the capability to alter data passing through it to and from an external interconnect fabric or device.

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